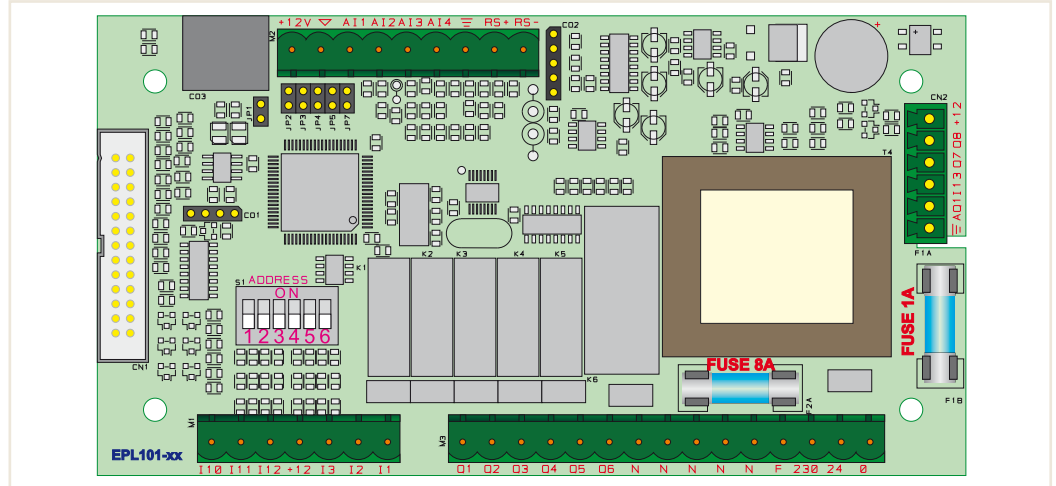
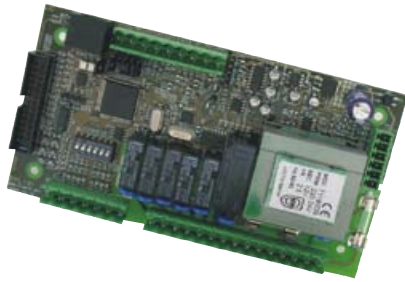


## WIRING PLAN



## EPL101

## PLC board 11 Inp / 9 Out

## Main features

Size	151 x 80 mm
Power supply	24/230 Vac $\pm 15\%$ 50/60 Hz transformer with double primary
Consumption	4W
Operating conditions	Temperature 0-45 °C, humidity 35..95 RH%
Weight	Approx. 280 gr.
Wiring harness	Extractable terminal bloks or Plug
Connection for display	(Optional) 26 poles connector for customized displays (7 segments)

## Inputs

Analogue	4 selectables for TC, K, J, S, R, T, E, PT100, Ni100, 0/4..20 mA, 0/1..10 V (for more details see technical documentation)
Digitals	6 inputs PNP + 1 input NPN
Inputs for Encoders	2 Digitals (overlapped to input PNP) for bi-directional encoder (1Khz), 1 Digital (overlapped to input NPN) for mono-directional encoder (2 KHz)

## Outputs

Digitals	5 Relays 5A resistive charge + 1 relay 16A resistive charge
Analogue	1 0..5Volt 8 Bit for command
Open- Collector	2 outputs max 20mA
Communication ports	1 serial RS232 on plug + 1 serial RS485 on Plug or on extractable terminal block

## Software features

Programming	Pixsys PLprog software, Ladder diagrams; 128 marker (logic relays), 64 bistables, 96 timer 16 bit, 32 up-down counters, mathematic and logic functions, rescale functions, contacts on bit
Communication protocols	Modbus RTU master / slave; Free-Port mode for Modem protocols or proprietary devices
Memory	64Kbyte Flash for programming, 200 words Ram, 1000 words EEPROM, external data memory MMC 32K words optional
Clock	Real-Time clock with Back-up battery (optional)
Analogue input control algorithms	P, PI, PID, PD

## Ordering codes

EPL101-1AB	PLC 4 An. Inp. + 6 Digital PNP + 1 NPN + 6 relays + 2 outputs Open-Collector 20mA + 1 output 0..5 Volt (8 bit)
KIT-MORS-EPL101	Kit Terminal blocks for PLC board EPL101
2100.30.010	M.c.Upgrade Series PL250-PL260-PL300-EPL101
2100.30.011	Expansion Memory 32K PL250
EPL101-RTC	Clock for EPL 101
SB-TERMS	LED Display for EPL101 (5 Digits, 6 keys, 5 LED)